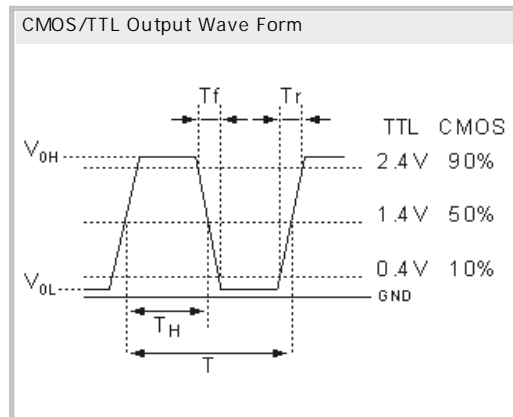
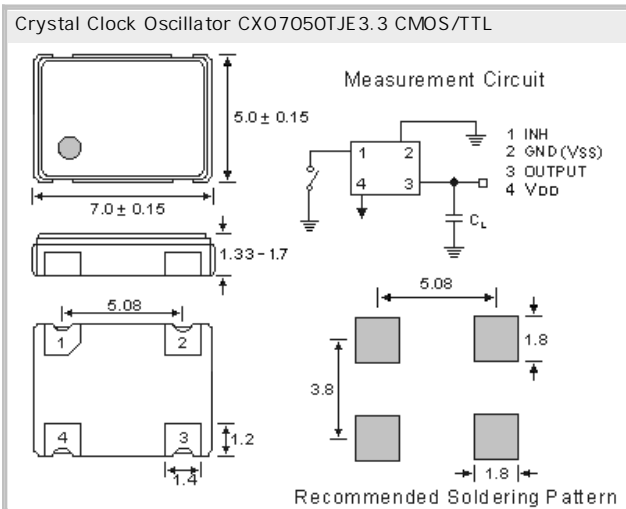


Crystal Clock Oscillator CXO7050TJE3.3 3.3V LVCMOS, Low cost, Phase Jitter 48 fsec.

- SMD in ceramic case (7.0 x 5.0 x 1.7) mm
- Tri-State Enable / Disable
- LVCMOS Square Wave
- on Tape & Reel (Tape 16mm)
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



## Specifications

Holder Type:	Crystal Clock Oscillator CXO7050TJE3.3 3.3V LVCMOS
Frequency Range:	5.0MHz ~ 50 MHz
Frequency Stability at 25°C:	± 50 ppm
Operating Temperature Range:	-40°C to +85°C, Inclusive of 25°C tolerance, operating Temp., Supply Voltage, & Load, aging, shock and vibration)
Storage Temperature:	-55°C to +150°C
Power Supply Voltage (Vdd):	+ 3.3V Vdd ± 10 %
Supply Voltage vs Freq. Sensitivity:	± 1.0 ppm (max.)
Maximum Supply Current:	7mA max. (typical), 10mA max.
Output Load:	15pF CMOS
Output Symmetry (Duty Cycle):	45/55%
Output Voltage (VOH) (VOL):	90% of Vdd min. / 10% of Vdd max.
Rise/Fall Time (10% to 90% Vdd):	10 ns max. 1.5 ns typical
Start Up Time:	0.8 ms typical < 5 ms max.
Tri-state Function Pin 1:	High Enable
Output Disable/Enable Time:	Enable : 1ms (max.) / Disable: 200 ns (max.)
Phase Jitter 49.152 MHz(12kHz -20MHz)	48 fsec. typ at 3.3 Vdd ; 118 fsec. typ. at 1.8 Vdd
SSB Phase Noise (3.3V):	-91dBc/Hz @ 10Hz, -126dBc/Hz @ 100Hz, -141dBc/Hz @ 1kHz -153dBc/Hz @ 10kHz, -166dBc/Hz @ 100kHz, -171dBc/Hz @ 1MHz
Aging:	< ± 3ppm max. for the first year; 2ppm max. per year thereafter
Moisture sensitivity level:	MSL= 1 per IPC/JEDEC J-STD-020 C
Reflow Condition:	260°C max for 10 sec.

## GERMANY:

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