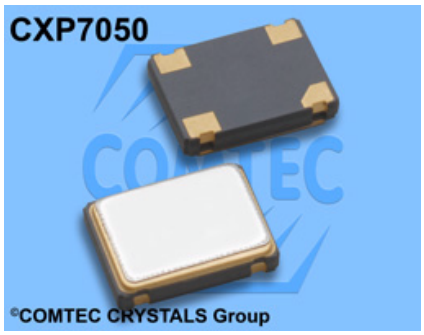
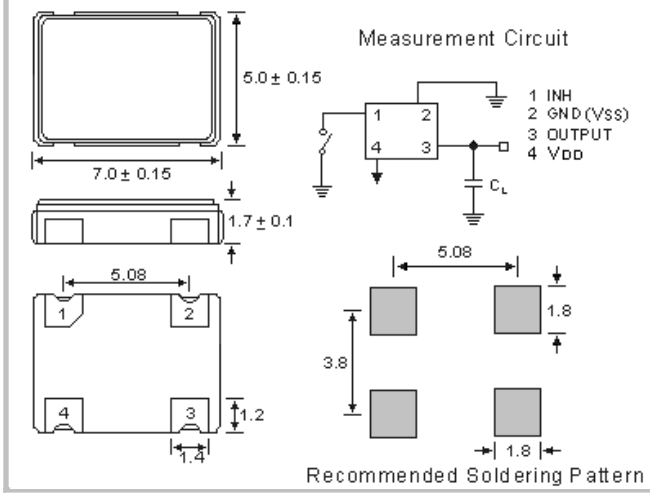


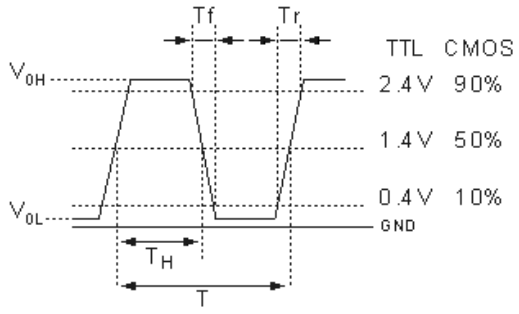

Programmable Crystal Clock Oscillator
CXP7050TM2.5, 2.5V 1 -166MHz LVCMOS

- SMD in ceramic case (7.0 x 5.0 x 1.7) mm
- Tri-State Enable / Disable
- CMOS/TTL Square Wave
- on Tape & Reel (Tape 16mm) or on trays
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies

Programmable Crystal Clock Oscillator CXP7050TM2.5




CMOS/TTL Output Wave Form




## Specifications - Product No. G032000000KTSUPN43AB

| | |
|---|---|
| Holder Type: | Programmable Crystal Clock Oscillator CXP7050TM2.5 2.5V LVCMOS |
| Frequency: | 32.000000 MHz |
| Frequency Stability at 25°C: | ± 25.0 ppm |
| Operating Temperature Range: | ± 25.0 ppm over -40°C to +85°C (incusive of 25°C tolerance, ±10% input voltage variation, load change, aging, shock and vibration ) |
| Storage Temperature: | -55°C to +150°C |
| Power Supply Voltage (Vdd): | + 2.5V D.C. ± 10% |
| Maximum Supply Current: | 13.0 mA |
| Max. Supply Current 25/200MHz: | 2.5V/3.0/13mA (15pF) |
| PLL Off: 25MHz Supply Current: | 2.5V 2.5mA (15pF) |
| PLL On 200MHz Supply Current: | 2.5V 12.5mA (15pF) |
| Rise/Fall Time: | 3.0ns typ. (25MHz, PLL off)<br>1.5ns typ. (200MHz, PLL off) |
| Output Load: | 15 pF CMOS |
| Start Up Time: | 5ms max. |
| Output Symmetry (Duty Cycle): | 50% ±5% (measured at 50% Vdd) |
| Period Jitter, peak to peak: | 70ps typical |
| Low Phase Noise (typical): | -114dBc/Hz at 1KHz offset (133MHz) |
| Tri-state Function Pin 1 (option): | Pin 1 = H or open.... Output active at pin 3<br>Pin 1 = L.... high impedance at pin 3 |
| Output Disable/Enable Time: | 150 ns |
| Aging: | < ±5ppm max. / year (max.) |
| Reflow Condition: | 260°C max. for 10 sec. |

GERMANY:
COMTEC CRYSTALS GmbH · Sultenstrasse 12-14
85586 Poing / GERMANY
Phone +49 8121 778160 · Fax +49 8121 778177
e-Mail info@comtec-crystals.com
Internet: http://www.comtec-crystals.com
Subject to change without prior notice.

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FRANCE:
COMTEC CRYSTALS SARL · 23, rue du Faucon
67500 Haguenau / FRANCE
Phone +33 388 732162 · Fax +33 388 730118
e-Mail sales@comtec-crystals.com
Internet: http://www.comtec-crystals.com
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