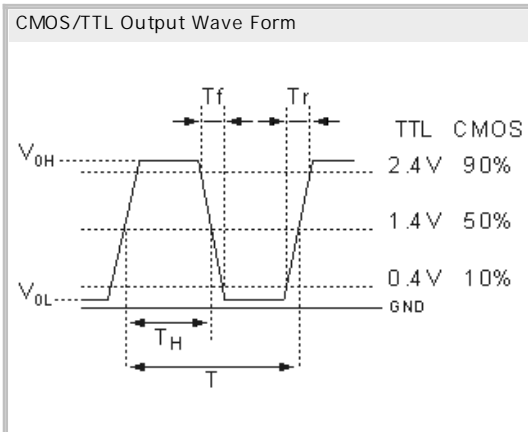
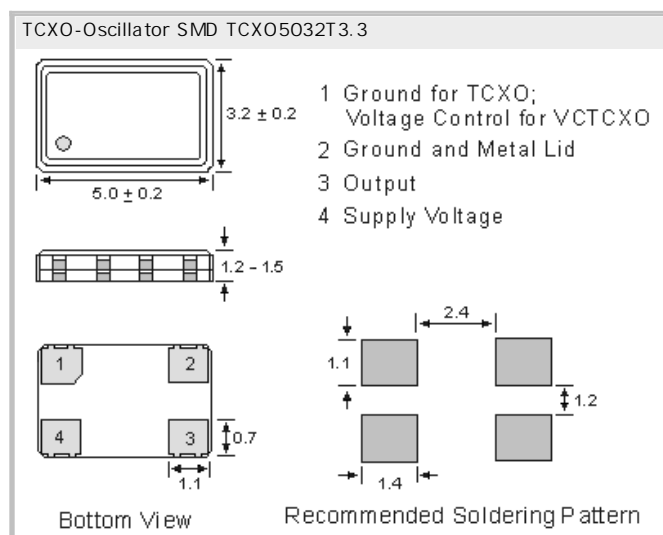




TCXO-Oscillator SMD TCXO5032T3.3 3.3V CMOS/TTL

- Temperature Compensated Crystal Oscillators
- SMD in ceramic case (5.0 x 3.2 x 1.5) mm
- CMOS/TTL Square Wave Output; Wave form code is " T "
- on Tape & Reel
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



Specifications - Product No. G01000000CEYUPN43JA

Holder Type:	TCXO5032T3.3 3.3V (Voltage code is " 3.3 ")
Frequency:	10.000000 MHz
Input Voltage:	+ 3.3V ± 10%
Frequency Stability:	± 1.5 ppm at 25°C
Freq. Stability vs Temperature:	± 1.0 ppm from -40°C to +85°C of 25°C tolerance, operating Temp., Supply Voltage, & Load, aging, shock and vibration)
Freq. Stability vs Aging:	± 1.0 ppm, first year at 25°C
Freq. Stability vs Voltage Change:	± 0.3 ppm, for a ± 5% input voltage change
Freq. Stability vs Load Change:	± 0.3 ppm, for a ± 10% load condition change
Maximum Supply Current:	6.0 mA max.
Output Load:	15pF
Output Logic Levels:	Logic High "1": 90% Vdd min. // Logic Low "0": 10% Vdd min.
Output Symmetry (Duty Cycle):	40/60% ; Measured at 50% Vdd
Rise/Fall Time CMOS:	10.0ns (max.) Measured between 20% and 80% Vdd of the wave form (CL = 15pF)
Start Up Time:	10 ms (max.), 5ms (typ.)
Phase Noise (10.00MHz 3.3V):	- 96dBc/Hz at 10Hz offset -122dBc/Hz at 100Hz offset -138dBc/Hz at 1kHz offset -145dBc/Hz at 10kHz offset -150dBc/Hz at 100kHz offset
Storage Temperature:	-55 to +105°C
Reflow Condition:	260°C max. for 10 sec.

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