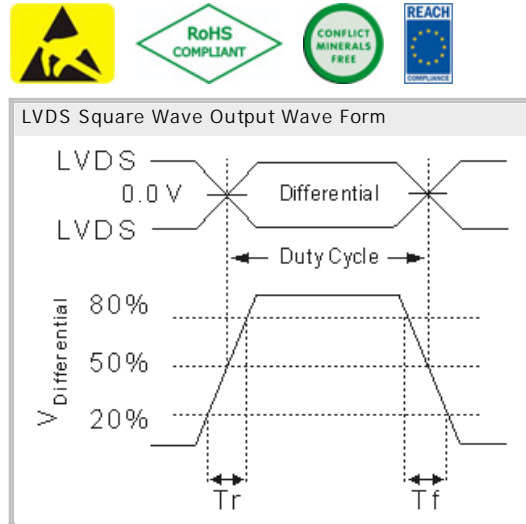
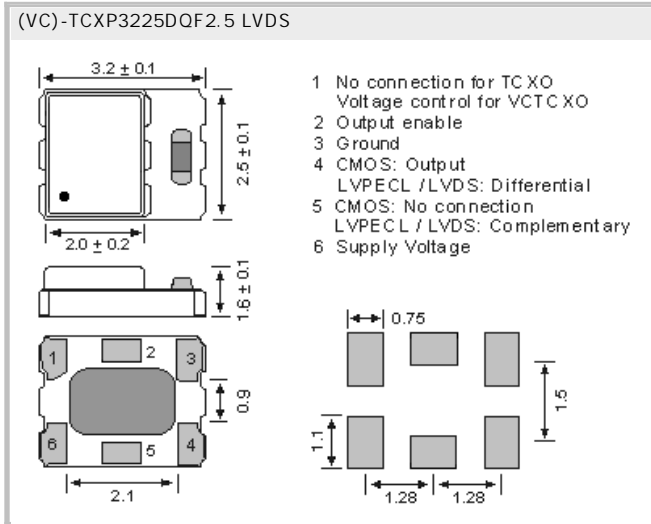


(VC)-Temperature Compensated Crystal Oscillator  
(VC)-TCXP3225DQF2.5 LVDS 2.5V 0.8pS Phase Jitter

- SMD in ceramic case (3.2 x 2.5 x 1.6) mm
- Tri-State Enable / Disable
- LVDS Differential
- on Tape & Reel
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



## Specifications

Holder Type:	Voltage Control Temperature Compensated Crystal Oscillator , Type (VC)-TCXP3225DQF2.5 LVDS 2.5V
Frequency Range:	10.0 MHz ~ 1.450 MHz
Initial Calibration Tolerance:	$\pm 1.0$ ppm max. at $25^\circ\text{C} \pm 2^\circ\text{C}$
Frequency Stability Codes ( FSC):	
( FSC) Temperature (ref to $+25^\circ\text{C}$ ):	$\pm 0.5$ ppm; $\pm 2.5$ ppm over $-30+85^\circ\text{C}$ ; $\pm 1.0$ ppm over $-40+85^\circ\text{C}$ (Inclusive Operating Temp., Supply Voltage, & Load)
( FSC) Aging:	$\pm 1.0$ ppm max. / year (max.)
( FSC) Voltage Change:	$\pm 0.2$ ppm max. , for a $\pm 5\%$ input voltage change.
( FSC) Load Change:	$\pm 0.2$ ppm max. , for a $\pm 10\%$ load condition change.
Power Supply Voltage (Vdd):	+ 2.5V D.C. $\pm 5\%$ ( voltage code " 2.5 " )
Output Symmetry (Duty Cycle):	45/55%
Start Up Time:	5 ms max.
Storage Temperature:	$-55^\circ\text{C}$ to $+150^\circ\text{C}$
Maximum Supply Current:	156MHz : 25 mA ; 600MHz : 30 mA ; 800MHz : 32 mA; 1 GHz : 36 mA
Current with Output Disabled:	18 mA ( typical )
SSB Phase Noise (Frequency 212.5 MHz):	-42dBc/Hz @ 10Hz, -87dBc/Hz @ 100Hz, -105dBc/Hz @ 1kHz -115dBc/Hz @ 10kHz, -118dBc/Hz @ 100kHz, -130dBc/Hz @ 1MHz, -151dBc/Hz @ 10MHz
Output Logic (VOH) (VOL):	1.4V typical, 1.6V max./ 1.1V typical, 0.9V min.
Control Voltage Function (CVF) Pad 1:	
(CVF) Linearity: $\pm 1\%$ typical $\pm 10\%$ max:	(CVF) Absolute Voltage: 4.0V max. // (CVF) Center+Range: $+1.5\text{V} \pm 1.0\text{V}$
(CVF) Transfer Function: Positive Transfer	(CVF) Input Impedance: 770 K typical // (CVF) Harmonics: -5.0 dBc max.
(CVF) Frequency Pulling Range:	$\pm 8$ ppm min.
Output Enable Funktion (OEF) Pad 2:	Output Enable Time/ Disable Time: 200nS max. / 50nS max.
(OEF) Integrated Phase Jitter:	0.8 pS typical ( 12 KHz to 20 MHz ) ; $< 150$ fS ( 1.875 KHz to 21 MHz )
Rise/Fall Time (20% to 80% Vdd):	0.2 ns typical $< 0.4$ ns max. ; $T_r/T_f$ : 20% $\leftrightarrow$ 80% wave form

## GERMANY:

COMTEC CRYSTALS GmbH · Sultenstrasse 12-14  
 8 5 5 8 6 P o i n g / G E R M A N Y  
 Phone +49 8121 778160 · Fax +49 8121 778177  
 e-Mail [info@comtec-crystals.com](mailto:info@comtec-crystals.com)  
 Internet: <http://www.comtec-crystals.com>  
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## FRANCE:

COMTEC CRYSTALS SARL · 23, rue du Faucon  
 6 7 5 0 0 H a g u e n a u / F R A N C E  
 Phone +33 388 732162 · Fax +33 388 730118  
 e-Mail [sales@comtec-crystals.com](mailto:sales@comtec-crystals.com)  
 Internet: <http://www.comtec-crystals.com>  
 sous réserve de modifications.