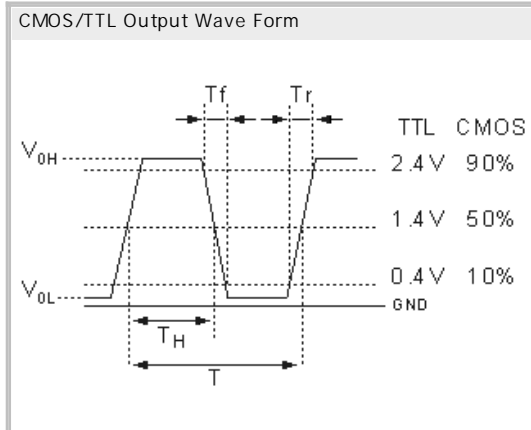
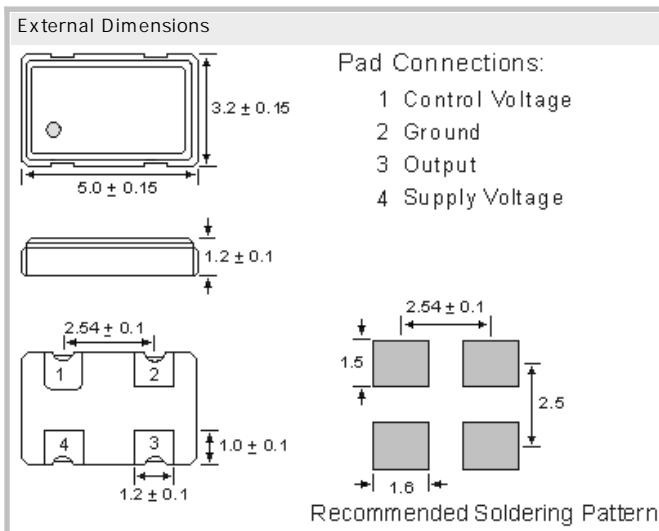


VCXO-Oscillator SMD VCXO5032T3.3-4pad 3.3V Voltage Controlled Crystal Oscillator

- Output Wave Form CMOS/TTL
- SMD in ceramic case (5.0 x 3.2 x 1.2) mm
- RoHS conform; Lead-free product
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



Specifications

Holder Type:	VCXO5032T3.3-4pad 3.3V (Voltage code is "3.3")
Frequency Range:	1.000 MHz to 50.0 MHz (Fun. frequency crystal used)
Initial Freq. Accuracy (at 25 °C):	To tune to the nominal frequency with Vc = +1.65V ± 0.2V
Frequency Stability at 25°C:	±25ppm (typ.) , ±50ppm, ±100ppm
Operating Temperature Range:	-10°C to +70°C / -40°C to 85°C, Storage Temp. -50°C to +105°C
Frequency Deviation Range:	±100ppm; ±150ppm
Power Supply Voltage (Vdd):	+3.3V DC ± 10%
Maximum Supply Current:	20 mA max.
Output Load:	2 TTL gates max. / CMOS 15pF (typ.)
Output "1" Level (VOH):	2.4V (min.) TTL 2.97V (min.) CMOS
Output "0" Level (VOL):	0.4V (min.) TTL 0.33V (max.) CMOS
Output Symmetry (Duty Cycle):	Standard 50% ±10%, Option 50% ±5%
Voltage Control:	1.65V DC Center / 0.3V to 3.0V Range
Linearity:	6% typical; 10% max.
Rise (Tr) / Fall Time (Tf):	(Tr) 6ns (max.); 4ns(typ.) Measured between 0.4V and 2.4V (Tf) 6ns (max.); 4ns(typ.) Measured 20% to 80% of wave form
Integrated Phase Jitter:	1 ps max. (12 kHz to 20 MHz)
Phase Noise (27MHz at 3.3V):	-75dBc/Hz at 10Hz offset -145dBc/Hz at 10kHz offset -104dBc/Hz at 100Hz offset -152dBc/Hz at 100kHz offset -132dBc/Hz at 1kHz offset -150dBc/Hz at 1MHz offset
Start Up Time:	10 ms (max.), 5ms (typ.)
Aging:	±3 ppm per year (max.)
Reflow Condition:	10 sec. max. at 260°C

GERMANY:

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