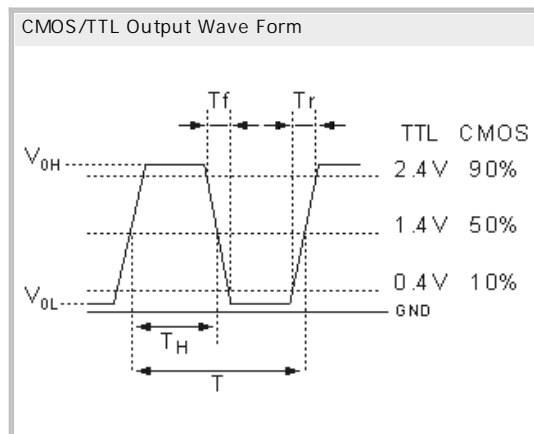
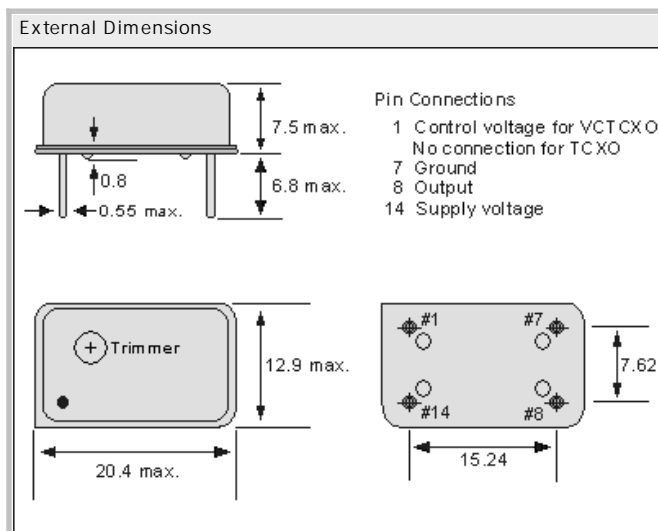


VCTCXO-Oscillator VCTCXO-15T5.0 DIL 14 5.0V  
Mechanical Frequency Tuning

- Voltage Control Temp. Compensated Crystal Osc.
- DIL in metal case (20.2 x 12.8 x 7.5) mm, 50pcs on a tray
- CMOS/TTL Square Wave Output; Wave form code is " T "
- RoHS conform; Lead-free product
- Available in many standard and special frequencies
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave



## Specifications

Holder Type:	VCTCXO-15T5.0 5.0V (Voltage code is " 5.0 ")
Frequency Range:	1.25 MHz to 156.0 MHz
Standard Frequencies (Part list):	10.0, 12.8, 13.0, 14.4, 15.36, 16.384, 19.2, 19.44, 19.68, 25.0, 20.0, 27.0, 38.88, 40.0, 77.76, 125.0, 155.52 MHz
Input Voltage:	+ 5.0V $\pm$ 10%
Frequency Stability:	$\pm$ 0.3ppm to $\pm$ 5.0ppm
Freq. Stability vs Temperature:	0° to 50°C ( $\pm$ 0.5ppm) to -40 to +85°C ( $\pm$ 1.0 to $\pm$ 3.0ppm)
Freq. Stability vs Aging:	$\pm$ 1.0 ppm, first year at 25 °C
Freq. Stability vs Voltage Change:	$\pm$ 0.3 ppm, for a $\pm$ 5% input voltage change
Freq. Stability vs Load Change:	$\pm$ 0.3 ppm, for a $\pm$ 10% load condition change
Frequency Tuning:	$\pm$ 3ppm min. by built-in mechanical trimmer
Current Consumption:	5 to 50 mA (Freq. dependent)
Voltage Control:	$\pm$ 6ppm min. (+1.5V $\pm$ 1.0V), positive Slope
Output Load:	15pF
Output Voltage Levels:	Logic High "1" : 4.2Vmin. (TTL), 90% Vdd min. (CMOS) Logic Low "0" : 0.5Vmin. (TTL), 10% Vdd min. (CMOS)
Output Symmetry (Duty Cycle):	40/60% normal, 45/55% tight at 50%
Rise/Fall Time CMOS:	10.0n sec. max.; 20% $\leftrightarrow$ 80% of the wave form
Start Up Time:	10 ms (max.); 5ms (typ.)
Phase Noise (16.384MHz):	-90dBc/Hz at 10Hz, -122dBc/Hz at 100Hz, -138dBc/Hz at 1kHz, -145dBc/Hz at 10kHz, -150dBc/Hz at 100kHz
Storage Temperature:	-40 to +85°C
Reflow Condition:	260°C max. for 10 sec.

### GERMANY:

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