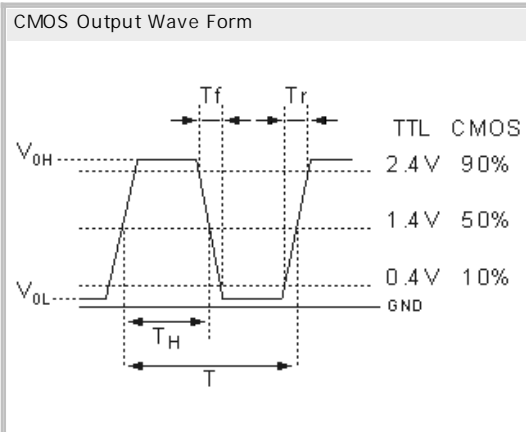
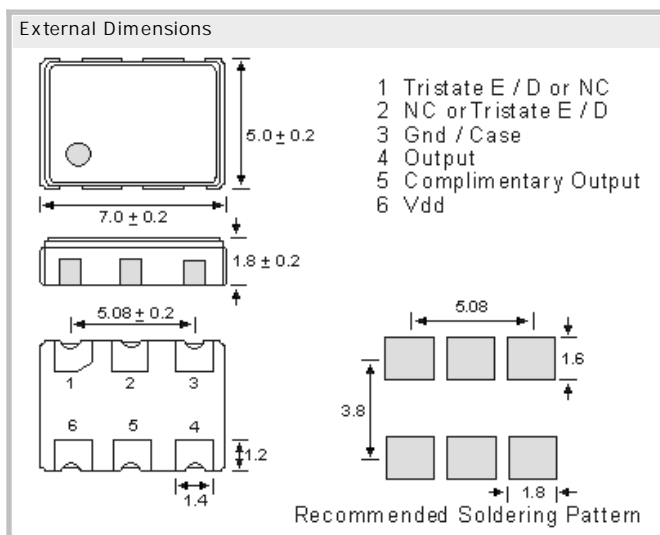


Clock Oscillator CXO7050TW3.3-1/2, 3.3V, low jitter, Low cost

- SMD in ceramic case (7.0 x 5.0 x 1.8) mm
- Tri-State Enable / Disable on pad No. 1 or 2
- LVCMOS Square Wave Output Wave Form
- High Q fundamental crystal + low jitter multiplier circuit
- RoHS conform; Lead-free product; on Tape (16mm) & Reel
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies



## Specifications

Holder Type:	CXO7050TW3.3-1/2; 3.3V(Voltage code is "3.3"); Tri-State on pad 1 or 2
Frequency Range:	125.01 MHz to 800.0 MHz
Frequency Stability at 25°C:	± 20 to ± 100 ppm
Operating Temperature Range:	-20° C to + 70° C, -40° C to + 85° C
Storage Temperature:	-55° C to + 150° C
Power Supply Voltage (Vdd):	+ 3.3V D.C. ± 5%
Maximum Supply Current (15pF load):	40mA max.
Output Logic Levels:	High "1" 90% of Vdd-min, Low "0" 10% Vdd-max.,
Output Symmetry (Duty Cycle):	50% ± 5% max. (measured at 50% Vdd)
Load:	15pF
Rise/Fall Time:	1.2ns typical, (0.3V<-> 3.0V, 15pF load)
Start Up Time:	10 ms max.
Tri-state Function Pin 1 (or 2):	Tri-state Enable High. No connection or 70% of Vdd min. is applied to a Tri-state pad to enable both outputs. 30% of Vddmax. to disable both outputs (high impedance).
Phase Jitter (12 kHz to 20 MHz):	2.6 ps typ., 4 ps max. for 155.52MHz
Phase Noise (typical):	-65dBc/Hz @ 10Hz, -95dBc/Hz @ 100Hz, -120dBc/Hz @ 1kHz -125dBc/Hz @ 10kHz, -121dBc/Hz @ 100kHz, -120dBc/Hz @ 1MHz
Input Static Discharge Protection:	2 kV (min.)
Aging:	< ± 3ppm max. for the first year; 2ppm max. per year thereafter
Reflow Condition:	260° C max for 10 sec.

### GERMANY:

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