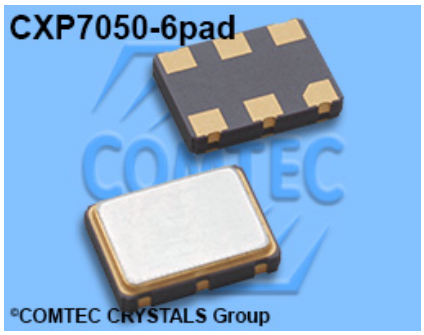
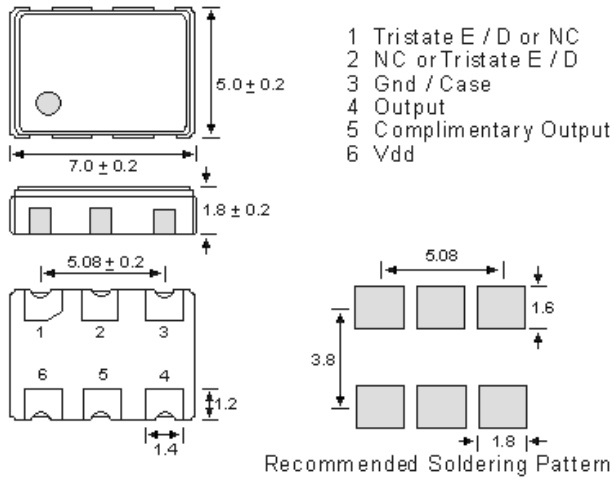


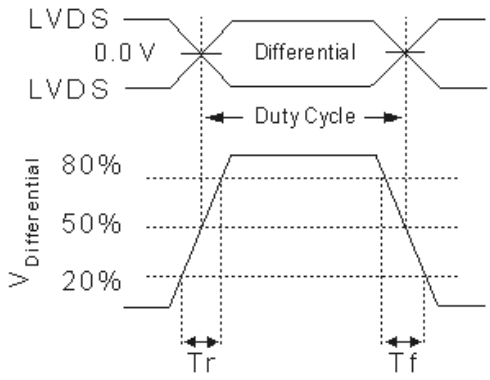
Clock Oscillator LVDS Differential, with PLL
CXP7050DQF3.3, 3.3V, 1.0ps typical Phase Jitter

- SMD in ceramic case (7.0 x 5.0 x 1.8) mm
- Tri-State Enable / Disable on pad No. 1
- LVDS Square Wave Output Wave Form
- High Q fundamental crystal + low jitter multiplier circuit + ultra low jitter multiplier circuit
- RoHS conform; Lead-free product; on Tape (16mm) & Reel
- Vibration: MIL-STD-202F method 204, 35G, 50 to 2000 Hz
- Shock: MIL-STD-202F method 213B, test cond. E, 1000GG 1/2 sine wave
- Available in many standard and special frequencies

Clock Oscillator LVDS, CXP7050DQF3.3



LVDS Square Wave Output Wave Form



Specifications - Product No. G371000000LJCUPN43BB

Holder Type / Voltage (Vdd):	CXP7050DQF3.3; + 3.3V D.C. ± 5%; Tri-State on pad 1
Frequency:	371.000000 MHz
Frequency Stability at 25°C:	± 50.0 ppm
Operating-//Storage -Temperature Range	± 50.0 ppm , -40°C to +85°C
Maximum Supply Current:	
Output Logic:	"High",1: 1.4V(typical); 1.6V (max.), RL = 100 Ohm "Low ",0: 0.9V (min); 1.1V (typical), RL = 100 Ohm
Output Voltage Swing:	250mV min., 350 typ., 450mV max., RL = 100 Ohm
Load:	100 Ohm between output and complimentary output
Rise (Tr)/Fall Time (Tf):	0.2ns typical; 0.5ns max. (20%Vdd<->80% of the LVDS wave form)
Start Up Time:	5 ms typical; 10ms max.
Tri-state Function Pin1(or2)No Connect.:	Differential LVDS and complimentary LVDS outputs
Tri-state Disable:	Both outputs are disabled (high impedance) when the Tri-state pad taken below 0.45°C Vcc referenced to ground (threshold) Oscillator is always On. Only Buffer stage is disabled. Disable current:50µA max.(at0.0V), Disable Time 10ns (max.)
Tri-state Enable:	At disabled mode, both outputs are enabled when the Tri-state pad taken below 0.45°C Vcc referenced to ground (threshold) Enable time: 10ms + one period of the output frequency (max.)
Phase Jitter:	1.0 ps typ. (12 kHz to 20 MHz); < 100 fs (1.875 MHz to 20MHz)
Phase Noise (156.250 MHz):	-55dBc/Hz @ 10Hz, -85dBc/Hz @ 100Hz, -109dBc/Hz @ 1kHz -116dBc/Hz @ 10kHz, -118dBc/Hz @ 100kHz, -139dBc/Hz @ 1MHz, -146dBc/Hz @ 5MHz
Aging:	< ±2ppm max. for the first year; ±10ppm max. over 10 years
Reflow Condition:	260°C max for 10 sec.

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