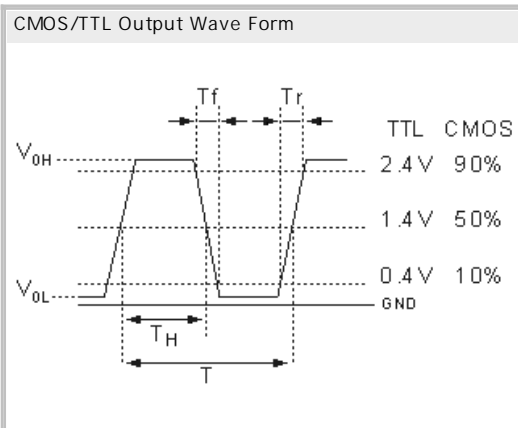
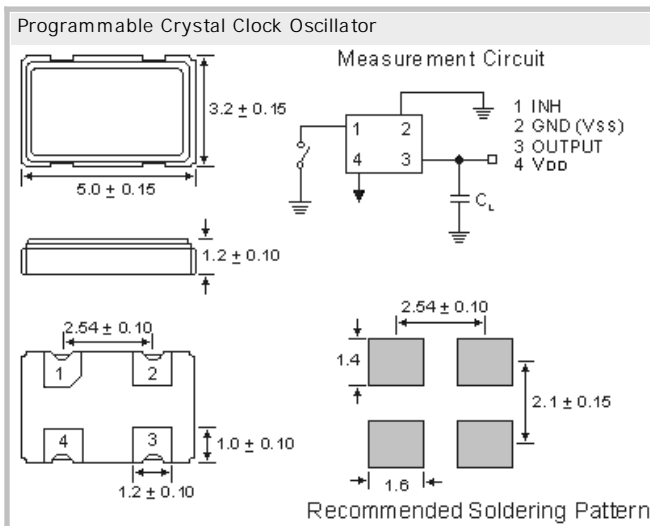


Programmable Crystal Clock Oscillator
5.0V 1-133MHz, 3.3V 1-100MHz, 2.7V 1-66MHz

- Short lead time. From one day to one week
- Ceramic case (5.0 x 3.2 x 1.2) mm
- CMOS/TTL Square Wave
- Available in many frequencies
- Packed in antistatic tubes
- RoHS conform; Lead-free product
- Available in many standard and special frequencies



Specifications - Product No. G00000000PESUPN43AA

Holder Type:	Programmable Crystal Clock Oscillator CXP5032TM3.3 (\pm 3.3V LVCMOS), CXP5032TM2.5 (\pm 2.5V LVCMOS), CXP5032TM1.8 (\pm 1.8V LVCMOS)
Frequency:	0.000000 kHz
Frequency Range:	5.0V 1-133MHz, 3.3V 1-100MHz, 2.7V 1-66MHz
Frequency Stability at 25°C:	\pm 25.0 ppm at 25°C
Operating Temperature Range:	\pm 25.0 ppm -40°C to +85°C (Inclusive of 25°C tolerance, operating temperature range, \pm 10% input voltage variation, load change, aging shock and vibration)
Storage Temperature:	-55°C to +125°C
Power Supply Voltage (Vdd):	+ 5.5V max. to 2.7V min.
Maximum Supply Current:	4.5-5.5V Vdd -> 45mA max., 3.0-3.6V Vdd -> 25mA max., 2.5-3.0V Vdd -> 20mA max.
Standby Current:	10 μ A typical, 50 μ A max.
Output Load CL:	CL for TTL levels: 4.5V -5.5V Vdd 40 MHz 50pF; 40 - 133 MHz 25pF CL for CMOS levels: 4.5V -5.5V Vdd 60 MHz 50pF; 66 - 133 MHz 25pF 3.0V -3.6V Vdd 40 MHz 30pF; 40-100 MHz 15pF; 2.5V -3.0V Vdd 66 MHz
Load Capacitance (CL):	15 -50pF CMOS
Output Symmetry (Duty Cycle):	40/60%
Output Voltage (VOH) (VOL):	90% of Vdd min. / 10% of Vdd max.
Rise/Fall Time (10% to 90% Vdd):	4 ns max. < 2.0 ns typical
Start Up Time:	10 ms max.
Tri-state (Option):	Pin 1 = H or open.... Output active at pin 3 Pin 1 = L.... high impedance at pin 3
Output Enable Time:	100 ns
RMS Jitter:	\pm 50ps max.
Aging:	< \pm 5ppm max. / year (max.)

GERMANY:

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